FIBER OPTIC MEMS SWITCH

for specialty fibers or Polarisation- Maintaining Fibers

OVERVIEW

The *sercalo sn* series are opto-mechanical switches for the most demanding applications in fiber optic sensor systems and instrumentation. The switch is available in 1x1, 1x2, 2x2 and 1x4 variants. The switch mechanism is available in either latching or non latching variants and has a very fast response time below 1 ms and below 1.5 dB insertion loss. The single mode switch is available for a number of specialty fibers covering design wavelengths such as 488 nm, 515 nm, 633 nm, 680 nm, 780 nm, 830 nm, 980 nm and 1064 nm. The 1x2 and 2x2 variants can also be made with polarisation maintaining PANDA fibers.

The miniature package withstands rugged environments and is well suited for direct mounting on printed circuit boards. The switch is qualified according to Telcordia GR 1221.

FEATURES

- reliable
- specialty fibers
- 1.5 dB insertion loss
- 1 ms response time
- low PDL
- 60 dB crosstalk
- miniature size
- 2x2, 2x1, 1x1 variants

APPLICATIONS

- Instrumentation
- Source selection

Contact:

Sercalo microtechnology ltd
Landstrasse 151, 9494 Schaan
Principality of Liechtenstein
Tel. +423 237 57 97   Fax. +423 237 57 48
http://www.sercalo.com   e-mail: info@sercalo.com

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DESCRIPTION

The **sercalo** switches are composed of an optical subsystem and an electrical driver interface. The optical switching function is realised by a silicon MEMS chip. In the latching *SL* variants a bistable suspension mechanism keeps the last selected state in power off. In the non-latching *SN* variants the switch returns into the bar state when electrical power is removed.

To operate the switch 5V and 0V are applied on the supply pins, which are used by the internal DC-DC converter to supply a high voltage for the actuator control. CMOS or TTL logic levels on the control pins switch the high voltage on the electrostatic actuator. To set the switch state in the *latching variant*, pin 2 respectively pin 3 are set to logic high (5V) for 10 ms and the corresponding switch state is selected. At rest pins 3 and 4 should be pulled to 0 V and must not be floating.

In the *non-latching variant* only pin 2 is used to set the state of the switch. To set the cross state pin 3 must be at logic high. When pin 3 goes to logic low, or at power off, the switch returns into the bar state.

BAR STATE (0 V)   CROSS STATE (5V)
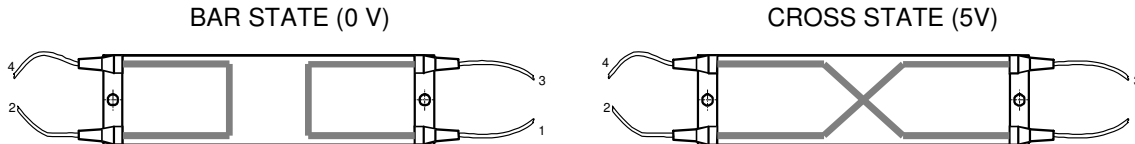
TECHNICAL SPECIFICATIONS

	Unit	Min	Typ	Max
Switch				
Wavelength Range	nm	Cut off	~Cut Off +200 nm	
Insertion Loss[1]	dB		0.7	1.5
Crosstalk	dB		75	60
Backreflection	dB		55	50
Polarisation Dependent Loss	dB		0.02	0.05
Polarisation Extinction Ratio[2]	dB	18	24	
Repeatability[3]	dB			0.001
Switching Time	ms		0.5	1
Durability	cycles		1 billion	
Package				
Voltage	V	4	5	5.25
Power Consumption	mW		5	10
Operation Temperature	℃	0		70
Storage Temperature	℃	-40		85
Size (L x W x H)	mm		40x16.5x9.5	

[1] for 1x2, 2x2 switch, fibers=5,6,7 excluding connector loss. Fibers 2,3 and 4 = 2 dB max. For 1x4 switch ILmax < 3.0 dB
[2] with Panda fibers for 7 and 9 only..
[3] value for constant temperature and polarisation

ORDERING INFORMATION

S N - 1x2 - 3 - N

Switch type
L = latching
N = non- latching

State monitor
 - = no monitor
TS= position monitor
(latching only)

Variants
2X2
1X4 (not for PM)
1X2 (no port 4)
1X1 (no ports 4,2)

Cut Off (design) Wavelength
3 = <450 nm (for 488 and 514 nm)
4 = <600 nm (for 633 and 680 nm)
5 = <750 nm (for 780 nm)
6 = <800 nm (for 830 nm)
7 = <970 nm (for 980, 1064, 1550 nm)
9 = <1250 nm (for 1310 and 1550 nm)

Polarisation Maintenance
PM=panda fiber, only for 970, 1310, 1550nm
- = single mode fiber

Fibre sleeve type
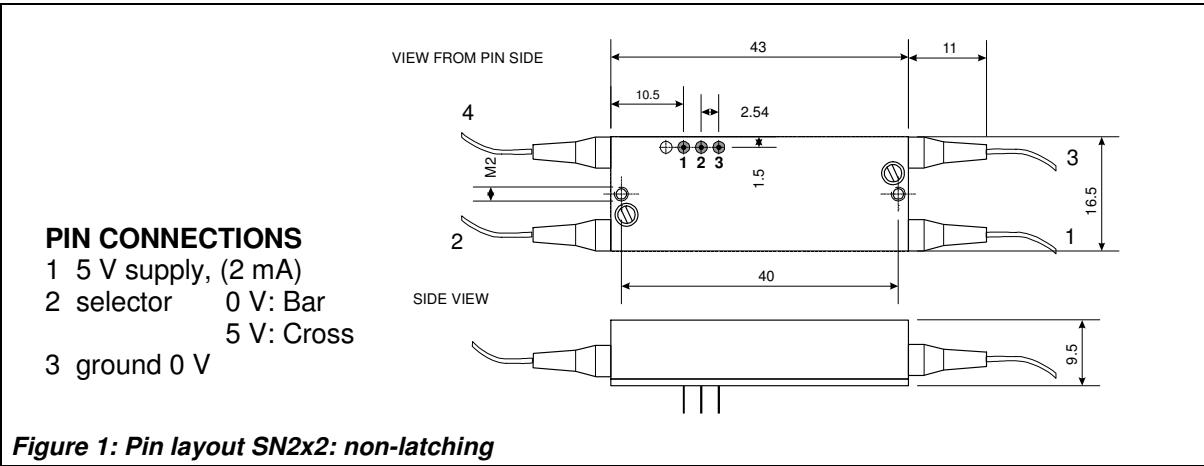N = loose tube 0.9mm
B = bare fibre 0.25mm

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VIEW FROM PIN SIDE

43   11

10.5   2.54

4

M2   1 2 3   1.5

3

16.5

PIN CONNECTIONS
1 5 V supply, (2 mA)
2 selector   0 V: Bar
              5 V: Cross
3 ground 0 V

2

SIDE VIEW   40

1

9.5

Figure 1: Pin layout SN2x2: non-latching

VIEW FROM PIN SIDE

43   11

8   2.54   1.5

out 4

M2   1 2 3 4

in 2   20.32   out 3   16.5

12.70

5 6

40

SIDE VIEW

9.5

PIN CONNECTIONS
1 5 V supply
2 bar select
3 cross select
4 ground 0 V
5 sensor output
6 sensor output
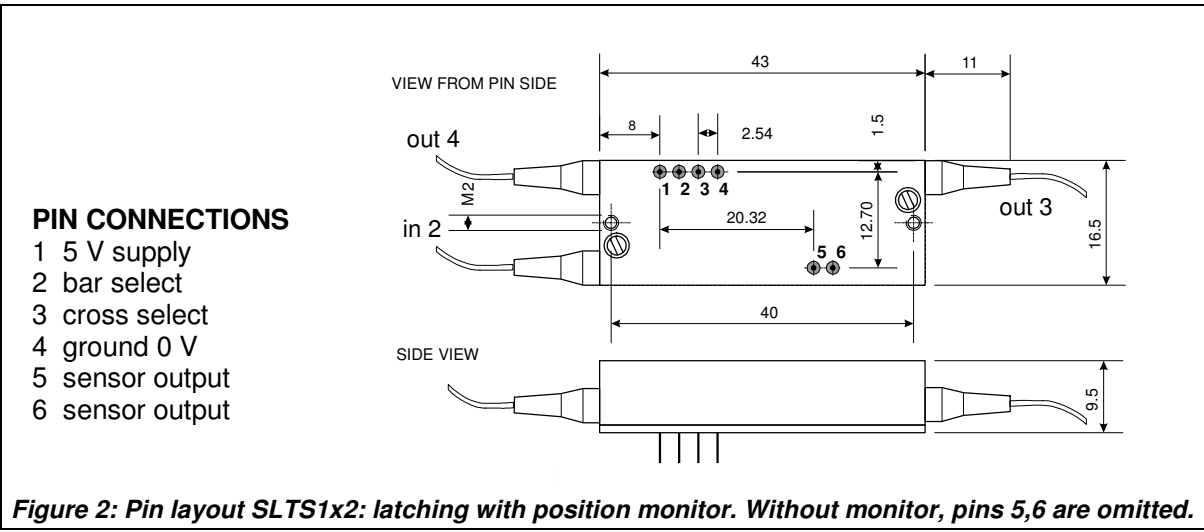
Figure 2: Pin layout SLTS1x2: latching with position monitor. Without monitor, pins 5,6 are omitted.

VIEW FROM PIN SIDE

3.0   10   5

0V S1 S2 S3 5 V supply

B

2.54   S6 S5 S4   2.54   A

70   65.0   42   59   65

input

Optical port selection:
1: 2ms pulse, 5V CMOS / TTL
0: 0V
x: 0V or 5V

D

C

40.0
50

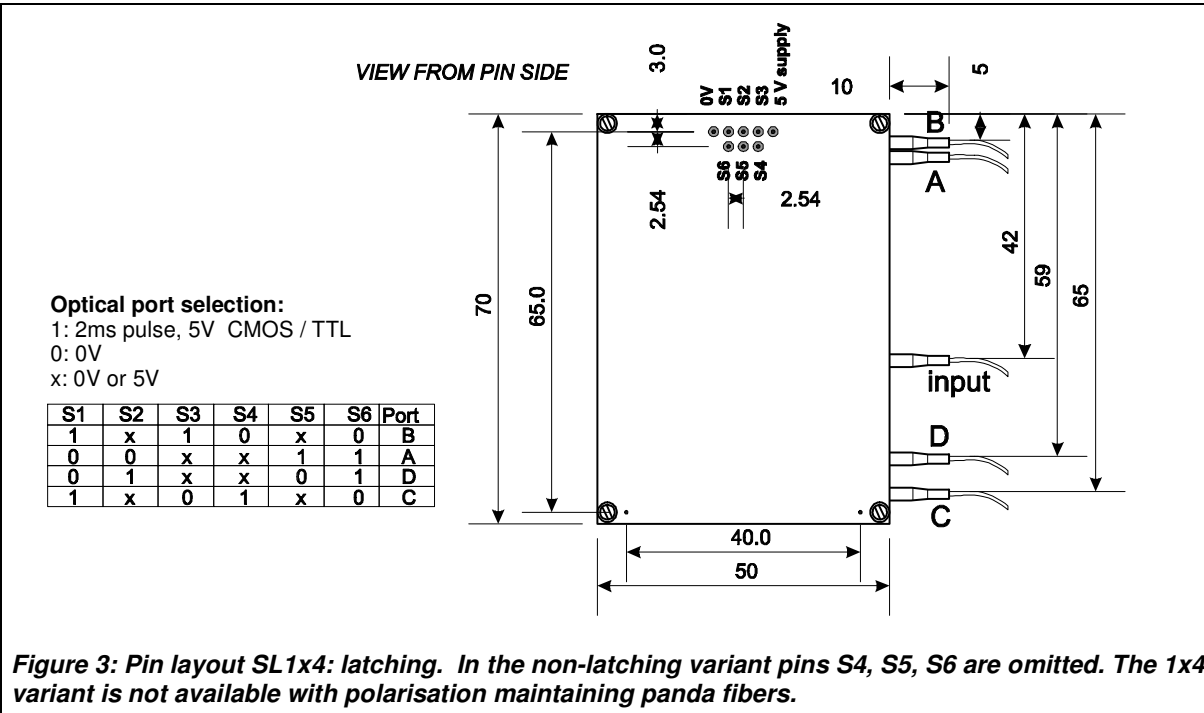| S1	S2	S3	S4	S5	S6	Port
1	x	1	0	x	0	B
0	0	x	x	1	1	A
0	1	x	x	0	1	D
1	x	0	1	x	0	C

Figure 3: Pin layout SL1x4: latching. In the non-latching variant pins S4, S5, S6 are omitted. The 1x4 variant is not available with polarisation maintaining panda fibers.

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